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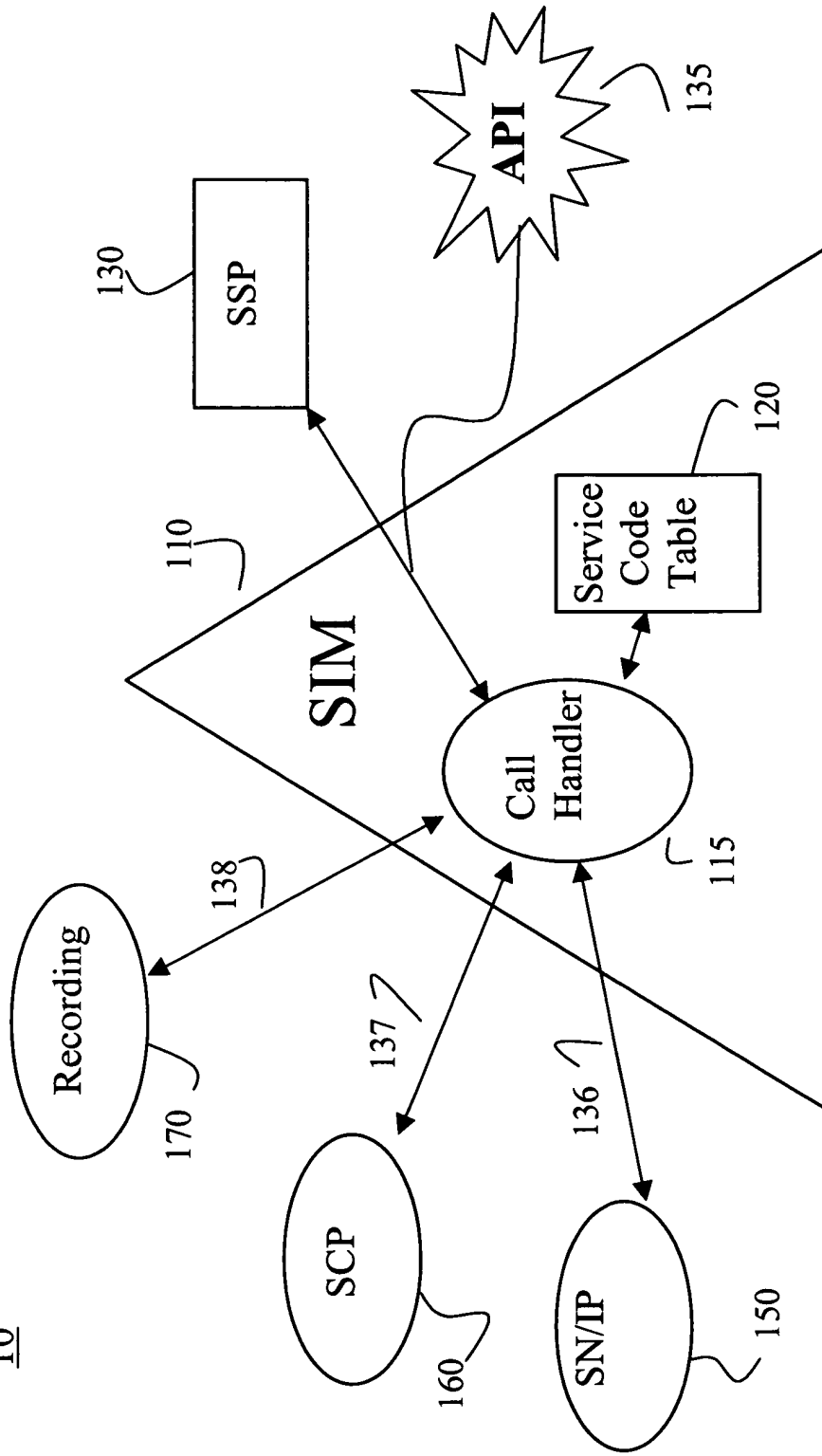


Fig. 1

FIG. 1 is a block diagram of a SIM system architecture.

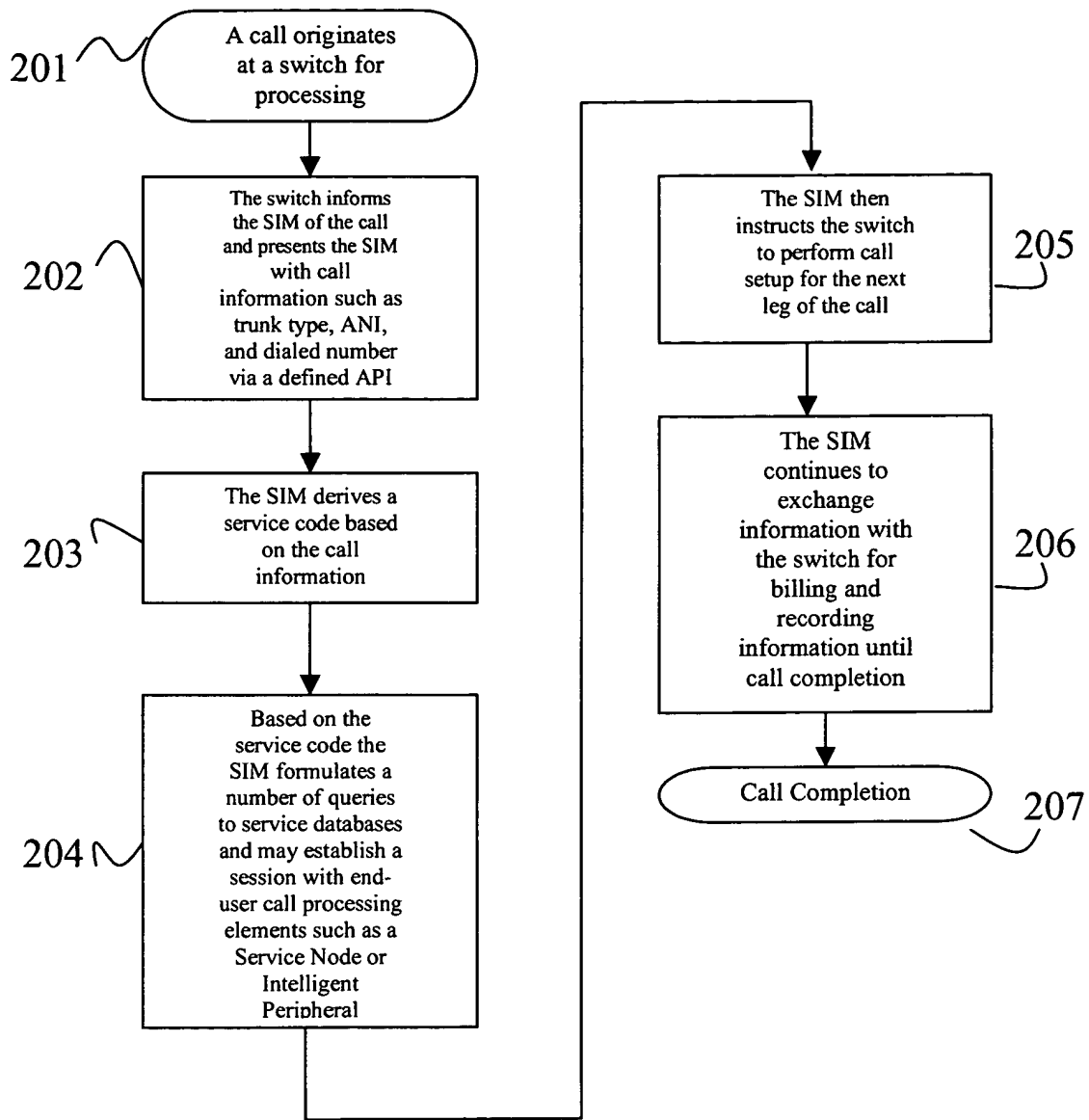


Fig. 2